

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
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Examiner		Art Unit		Page 1 of 1
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	B	US-2004/0110326	06-2004	Forbes et al.	438/149
	C	US-2005/0042796	02-2005	Wagner, Sigurd	438/065
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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*	V	Wang et al, "Curved Silicon Electronics," Materials Research Society Symp. Proc. Vol. 769 (2003)
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.